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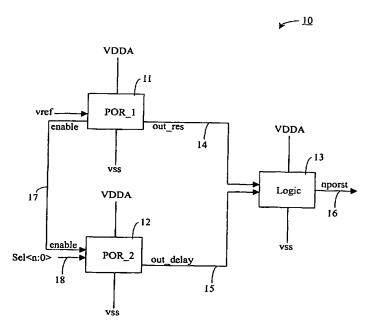
- AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
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(54) Title: CONTACT MEMBER FOR MANHOLE AND THE LIKE



(57) Abstract: A contact member adapted to be positioned at least partially between a cover and a frame of a manhole structure or the like is disclosed. The contact member has a first side adapted to be connected to the cover and a second side adapted to be connected to the frame and at least one compartment open to the surface of the contact member. The compartment(s) are positioned between the first side and the second side. Amongst others, the disclosed contact member allows for larger tolerances of the cover and/or frame dimension.

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- before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments
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INTERNATIONAL SEARCH REPORT

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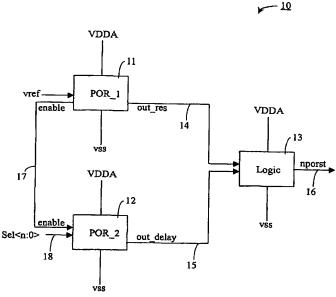
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(54) Title: POWER SUPPLY LEVEL MONITORING AND RESET GENERATION



(57) Abstract: Apparatus (10) for generating a logic signal (nporst) indicating that a supply voltage (VDDA) has reached a stable level. The apparatus (10) comprises a first unit (POR_1, 11) for comparing a reference voltage (vref) with a sub-voltage (input_plus) derived from the supply voltage (VDDA) in order to issue a first logic signal (out_res) when the sub-voltage (input_plus; Vtrl, Vsel) has reached the reference voltage (vref). A second unit (POR_2, 12;22) is provided for applying a delay in order to issue a delayed logic signal (out_delay). A logic unit (13) combines the first logic signal (out_res) and the delayed logic signal (out_delay) in order to provide the logic signal (nporst).

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